Advanced Hardware Design for Error Correcting Codes

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This book provides thorough coverage of error correcting techniques. It includes essential basic concepts and the latest advances on key topics in design, implementation, and optimization of hardware/software systems for error correction. The book's chapters are written by internationally recognized experts in this field. Topics include evolution of error correction techniques, industrial user needs, architectures, and design approaches for the most advanced error correcting codes (Polar Codes, Non-Binary LDPC, Product Codes, etc). This book provides access to recent results, and is suitable for graduate students and researchers of mathematics, computer science, and engineering.

• Examines how to optimize the architecture of hardware design for error correcting codes;

• Presents error correction codes from theory to optimized architecture for the current and the next generation standards;

• Provides coverage of industrial user needs advanced error correcting techniques.

Advanced Hardware Design for Error Correcting Codes includes a foreword by Claude Berrou.

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